

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	22766	active near matrix near3 display	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:43
L2	81882	TFT\$1 or (thin near film near transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:44
L3	523551	(interlayer near2 insulat\$3) or dielectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:45
L4	34576	pixel near electrode\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:46
L5	84977	(driver near circuit) or (peripheral near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:46
L6	166467	(active near2 circuit) or (switching near circuit) or (pixel near2 circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:47
L7	63928	NTFT or (n-channel near MOSFET) or NMOS or PTFT or (p-channel near MOSFET) or PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:49
L8	430	I1 and I2 and I3 and I4 and I5 and I6 and I7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/29 12:49